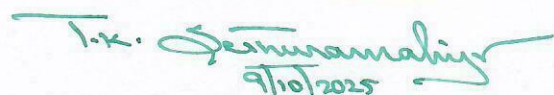
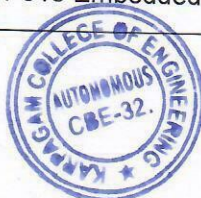


AUTONOMOUS END SEMESTER EXAMINATIONS – DECEMBER 2025 TIME TABLE
ME (VLSI Design) Programmes (2024 Batch)**Timings FN: 09.30 AM – 12.30 PM****Semester - III**

DATE / DAY	SESSION	COURSE CODE & TITLE
31.12.2025 Wednesday	FN	23VLR313 ASIC Design
03.01.2026 Saturday	FN	23VLR314 Analysis and Design of Analog Integrated Circuits
05.01.2026 Monday	FN	23VLP315 Hardware Design Verification Techniques
07.01.2026 Wednesday	FN	23VLO103 VLSI for Wireless Communication
09.01.2026 Friday	FN	23CUP313 Embedded System Integration


9/10/2025**CONTROLLER OF EXAMINATIONS**

Copy to:

- The Principal
- The Director - KTA
- The Dean (Academics)
- The Head – ECE – To inform the Students
- Exam Cell
- Office
- Notice Board
- File