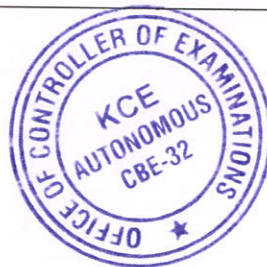


AUTONOMOUS END SEMESTER EXAMINATIONS – DECEMBER 2023 TIME TABLE
ME (VLSI Design) Programme (2023 Batch)

Timings FN: 09.30 AM – 12.30 PM

Semester - I

Date / Day	Session	Course Code / Course Name
24.01.2024 Wednesday	FN	23VLR102 Computer Aided Design for VLSI Circuits
29.01.2024 Monday	FN	23VLR103 Advanced Digital System Design
31.01.2024 Wednesday	FN	23VLR104 Testing of VLSI Circuits
02.02.2024 Friday	FN	23VLR105 Advanced Computer Architecture and Parallel Processing
05.02.2024 Monday	FN	23VLR106 Digital Signal Processing Integrated Circuits
07.02.2024 Wednesday	FN	23MRM101 Research Methodology and IPR



T.K. Senthuramalingam
 3/10/2023

CONTROLLER OF EXAMINATIONS

Controller of Examinations
 Karpagam College of Engineering
 (Autonomous)
 Coimbatore - 641 032.

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 The Head (ECE) – To inform the Students
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