

**One Day National Level Workshop
on
Digital Signal Processing using Scilab**

26th September, 2017

Registration Form

Name:.....

Qualification:.....

Designation:.....

Department:.....

Institution:

Mailing address:.....

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Phone:.....

Office:.....

Mobile:.....

Email:.....

Payment Details:

DD No:Date:.....

Amount:.....Bank:.....

Signature of Applicant

ORGANISING COMMITTEE

Chief Patron : **Dr. R. Vasanthakumar,**
Chairman & Managing Trustee,
Karpagam Educational Institutions.

Patron : **Shri. K. Murugaiah,**
Chief Executive Officer,
Karpagam Educational Institutions.

Chairman : **Dr. P. Vijayakumar,**
Principal.

Convener : **Dr.P.Karthigaikumar,**
Professor and Head,
Department of ECE.

CO-ORDINATORS

Dr.C.Priya,AP/ECE
Ms. L.Saranya,AP/ECE

For further enquiries and communication,

Contact :

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on
Digital Signal Processing using Scilab**



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Organized by

**Department of Electronics and Communication
Engineering**

(Accredited by NBA and NAAC-'A' Grade)

ABOUT THE COLLEGE

Karpagam College of Engineering was started in the year 2000, by Karpagam Charity Trust, promoted by renowned Philanthropist and Industrialist Dr. R. Vasanthakumar and has been conferred with autonomous status in the year 2007. The college offers nine Under Graduate Engineering programmes and five Post Graduates programmes and accredited by NAAC with 'A' Grade. The Institution aims to provide excellent learning facilities by imparting practical training, knowledge and skills to the youth. The college has signed MoUs with research organizations and industries to promote closer interaction in the areas of technology development, student training, curriculum updation and establishment of state-of-art-centers of excellence.

ABOUT THE DEPARTMENT

The department has experienced and qualified faculty members, well equipped laboratory such as DSP lab, MEMS Lab, Communication and Microwave Engineering Lab, LIC / Digital Lab, VLSI Design Lab, Networks and Simulation and Electron Devices Lab with availability of latest and updated version of software package. The department has also promoted different centre of excellence for MEMS Research and Development (CMRD), Centre for Signal Processing Research and Development (CSPRD), Centre for Embedded

system Research and Development (CESRD), Centre for Image and Video Processing Research and Development (IBRD), Centre for Research Communication and Wireless Network (CRCWN) and Centre for VLSI Design and Development (CVDD) for developing research activities in the field of Electronics and Communication Engineering.

OBJECTIVE OF THE WORKSHOP

The main objective of this workshop is to motivate the participants to pursue a carrier in the field of Signal processing. Signal processing concerns the analysis, synthesis, and modification of signals, which are broadly defined as functions conveying "information about the behavior or attributes of some phenomenon" such as sound, images, and biological measurements. A good understanding of signal processing will enable the participants to pursue research in the field of image processing and Wavelet processing.

KEY CONTENTS OF THE WORKSHOP

- Overview of Signals and Systems
- Introduction to Digital signal processing
- DFT and FFT Algorithms
- Design of FIR filters
- Design of IIR filters
- Hands on training using Scilab

HOW TO APPLY

- Send the registration form available in this brochure along with DD to the convener
- First Come First Serve basis.
- No Spot registration & Registration is restricted to 30 Participants.

PARTICIPANTS

- Students
- Faculty Members
- Members from Industries

IMPORTANT DATES

Last date for registration : 18.09.2017
Intimation of acceptance : 19.09.2017

REGISTRATION FEE

All Category: Rs. 300/-

PAYMENT

**By Demand draft may be drawn in favour of
"The Principal, Karpagam College of Engineering"
Payable at Coimbatore.**

VENUE

EB 406 - DSP LAB
Department of Electronics and
Communication Engineering