

# KARPAGAM COLLEGE OF ENGINEERING (Autonomous)

S.F.No.758,759,760 Othakkal Mandapam

Coimbatore - 641032. Tamil Nadu

## FACULTY PROFILE

Name	Dr. P. Karthigaikumar
Designation	Professor and Head
Department	Electronics and Telecommunication Engineering
Qualification	B.E. Electrical and Electronics Engineering M.E. Applied Electronics Ph.D. Information and Communication Engineering



### Experience

- July 1<sup>st</sup> 2014 to till date – working as a Professor and Head in Electronics and Telecommunication Engineering in Karpagam College of Engineering, Coimbatore, Tamil Nadu, India.
- June 1<sup>st</sup> 2013 to 30<sup>th</sup> June 2014 – Worked as a Professor in Electronics and Communication Engineering Department in Karpagam College of Engineering, Coimbatore, Tamil Nadu, India.
- 1<sup>st</sup> May 2012 to 15<sup>th</sup> May 2013 – Worked as a Associate Professor in Electronics and Communication Engineering Department in Karunya University, Coimbatore, Tamil Nadu, India.
- 1<sup>st</sup> January 2009 to 31<sup>st</sup> April 2012- worked as a Assistant Professor (SG) in Electronics and Communication Engineering Department in Karunya University, Coimbatore, Tamil Nadu, India.
- 3<sup>rd</sup> March 2008 to 31<sup>st</sup> Dec 2008 - worked as a Assistant Professor in Electronics and Communication Engineering Department in Karunya University, Coimbatore, Tamil Nadu, India.
- 1<sup>st</sup> Nov 2005 to 2<sup>nd</sup> March 2008 – worked as a Senior Lecturer in Electronics and Communication Engineering Department in Karunya Institute of Technology, Coimbatore, Tamil Nadu, India.
- 21<sup>st</sup> June 2000 to 31<sup>st</sup> Oct 2005 – worked as a Lecturer in Electronics and Communication Engineering Department in Karunya Institute of Technology, Coimbatore, Tamil Nadu, India.

### Publications in Journals

a) National	-	Nil
b) International	-	36
c) Conference	-	31

Books Published - Nil

No. of Doctorate Students Guided: Guiding for 10 Students at present.

Any Other Achievements

### **IEEE (ED) Coimbatore Chapter Secretary**

Received IETE K S Krishnan Award from IETE 2010.

Guest Editor for Special Issue “The boon and the bane of medical image analysis - An introspection” International Journal of Sensing and Imaging published by **Springer**. (2016)

Guest Editor for Special Issue “Researches and Challenges in green radio Communication: A Future World” International Journal of Wireless Personal Communications published by **Springer**. (2015)

Guest Editor for Special Issue “Hybrid Intelligent Techniques for Benchmark Functions and Real-World Optimization Problems” to the Mathematical Problems in Engineering Journal published by **Hindawi** Publisher.

Guest Editor for Special Issue “Challenges to IC Design in the 21st Century” to the International Journal of Circuits and Architecture Design (IJCAD) published by **Inderscience Publisher**. (2016)

Guest Editor for Special Issue “Issues and Challenges in Developing Biomedical and Image Processing Algorithms in the 21st Century” to the International Journal of Telemedicine and Clinical Practices (IJTMCP) published by **Inderscience Publisher**. (2016)

Guest Editor for Special Issue “Reconfigurable Computing in Wireless Networks” to the International Journal of Information and Communication Technology published by **Inderscience Publisher**.

Guest Editor for Special Issue “Issues and development on network security Algorithm” to the International Journal of Electronic Security and Digital forensics published by **Inderscience Publisher**.

Guest Editor for Special Issue “Innovative design method for smart concurrent systems” to the International Journal of High Performance System Architecture published by **Inderscience Publisher**.

Guest Editor for Special Issue “Challenges and Solutions in Wireless Network Security”  
International Journal of Information and Computer Security published by **Inderscience  
Publisher**.

Reviewer for Microelectronics	- Elsevier
Reviewer for Wireless Personal Communications	- Springer
Reviewer for Information and Computer Security Journal	- Inderscience
Reviewer for Information Security Journal	- Inderscience
Reviewer for International Journal of Information and Communication Tech	- Inderscience
Reviewer for International Journal of Electronics Fransis	- Taylor &
Reviewer for International Journal of Digital Forensics Fransis	- Taylor &
Reviewer for Security and Communication Networks	- Wiley
Reviewer for IETE Journal of Research	- IETE
Reviewer for EURASIP Journal on Image and Video Processing	
Editorial Board member of IJCEE, Singapore	
Editorial Board member of International Journal of Advanced Computer Research	
Editorial Board member of IOSR Journal of VLSI and Signal Processing (IOSRJVSP)	
Editorial Board member of International Journal of Advanced Computer Research (IJACR) - Accents	
Distinction in M.E	

## **Patent**

Filed a patent for the research titled “BUILDING QUALITY ANALYZER AND A LIFE PREDICTOR TO AVOID COLLAPSE /DISASTER””. Patent No: 5689/CHE/2014.

Filed a patent for the research titled “Secure FPGA & ASIC implementation of low power media security processor using combined watermarking and cryptography”. Patent No: 3542/CHE/2010.